

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 10010939	SERIAL NO. 09/095048
	APPLICANT Mesarina et al.	
	FILING DATE 06/29/01	GROUP

[illegible][illegible]

1	"SCHEDULING FOR REDUCED CPU ENERGY", Weiser et al., Xerox PARC, Palo Alto CA; First Symposium on Operating Systems Designs & Implementation (OSDI)
2	"POWER CONSCIOUS FIXED PRIORITY SCHEDULING FOR HARD REAL TIME SYSTEMS", Shin et al., Seoul Nat. University, Seoul, Korea
3	"SCHEDULING ALGORITHMS FOR MULTIPROGRAMMING IN A HARD REAL TIME ENVIRONMENT", Liu et al., Massachusetts Institute of Technology, CA Institute of Technology, Journal of the Assoc. for Computing Machinery, vol. 20, No.1, January 1973, pp. 46-61
4	"COMPARING ALGORITHMS FOR DYNAMIC SPEED-SETTING OF A LOW-POWER CPU", Govil et al., Computer Science Division, USC, Berkeley, CA
5	"POWER OPTIMIZATION OF VARIABLE VOLTAGE CORE-BASED SYSTEMS", Hong et al., Computer Science Dept., UCLA, LA, CA, Electrical Engineering Dept., UCLA

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REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

11002 U.S. PTO
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6	"A SCHEDULING MODEL FOR REDUCED CPU ENERGY", Yao et al., Xerox Palo Alto Research Center, Palo Alto, CA, 19955

Assad M. Hamez

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